

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

Applicant: Date-Gun LEE)	For: "Methods to Fabricate
)	Semiconductor Devices"
Serial No.: To be assigned)	
)	Group Art Unit: To be determined
Filed: December 19, 2003)	
)	Examiner: To be determined

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

This information disclosure statement is being filed with the application. Therefore, in accordance with 37 CFR §1.97(b), no certification or fee is required.

The Commissioner is authorized to charge any fee deficiency required by this paper to Deposit Account No. 50-2455. A copy of this paper is enclosed.

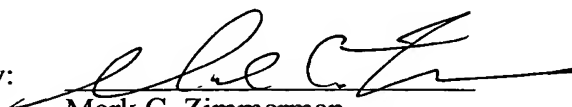
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December 19, 2003

By:



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Form PTO-1449 (Modified) INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>	U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No. 20067/OPP031055US	Serial No. To be Determined
		Applicant Date-Gun LEE	
		Filing Date 12/19/03	Group Art Unit TBD

U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01	6,524,931	2/25/03	Perera	438	437	
	A02	6,180,490	1/30/01	Vassiliev et al.	438	424	
	A03	6,074,927	6/13/00	Kepler et al.	438	400	
	A04	5,994,201	11/30/99	Lee	438	427	
	A05	5,976,951	11/2/99	Huang et al.	438	435	
	A06	5,721,173	2/24/98	Yano et al.	438	424	

FOREIGN PATENT DOCUMENTS

*Examiner Initials		Document Number	Publication Date	Country	Class	Subclass	Translation	
	B01						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	C01	
	C02	

EXAMINER	DATE CONSIDERED
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance <u>and</u> not considered. Include copy of this form with next communication to applicant.	